

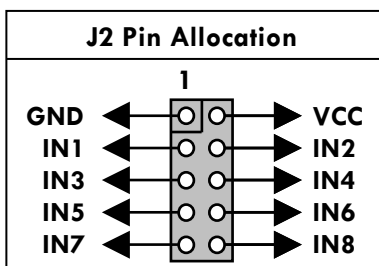
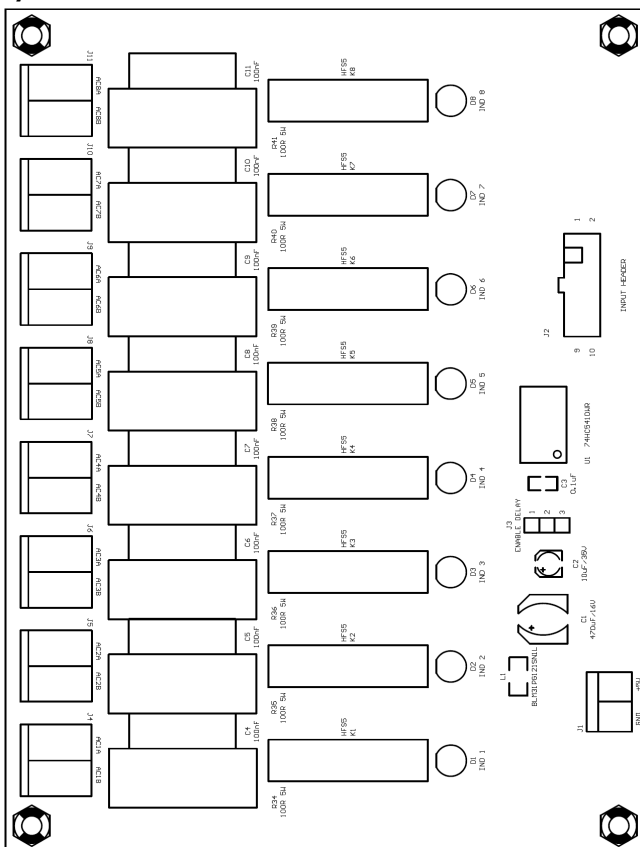
DT-I/O Solid State Relay Board

DT-I/O Solid State Relay Board is a module that consists of 8 Solid State Relay (SSR), each has a LED indicator to indicate the status of the SSR (on or off). Each SSR has 2 load connector pins.

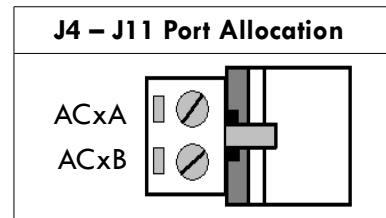
Specifications

1. SSR load range: 75 – 280 VAC, 0.1 – 2 A.
2. Requires +5 Volts DC power supply.
3. Input logic on the INPUT HEADER connector is compatible with TTL and CMOS level input.
4. There is a delay feature, which secures the devices connected to the SSR from an unstable input condition whenever the power supply is turned on.
5. Fully compatible with DT-51™ Low Cost Series and DT-AVR Low Cost Series.
6. Supports DT-51™ Minimum System (MinSys) ver 3.0, DT-51™ PetraFuz, DT-BASIC Series, and other control systems.

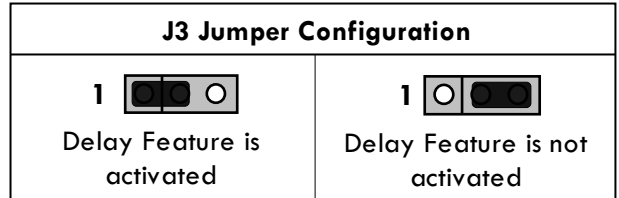
Layout



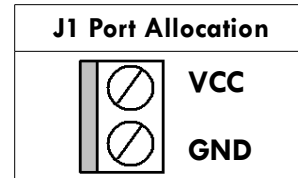
Input voltage (+5 VDC) for DT-I/O SSR Board can be connected to GND and VCC pins at J2 or J1. If INx pin is given a logic "1" (+5V DC), then SSR is active and LED lights up.



When the SSR is active (and is given a load), ACxA and ACxB will be connected. When SSR is not active, ACxA and ACxB is not connected. The installation of these connectors can be reversed (there is no polarity).



If the delay feature is activated, input condition won't affect SSR condition for a moment after power supply is turned on. This time delay (about 82 ms) serves to anticipate an unstable input condition when the power supply is turned on. After the time delay has passed then the SSR condition will match the input condition.

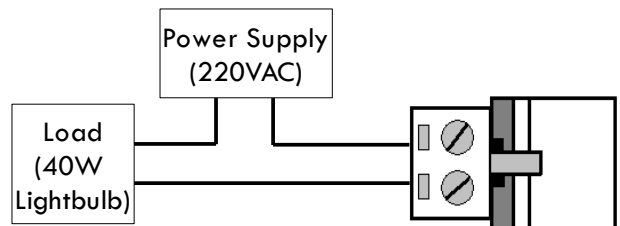


Important!

- If the input voltage +5 VDC has been connected via J1, don't connect the input voltage to J2. And vice versa.

Testing Procedure

1. Connect the INPUT HEADER DT-I/O SSR Board with the DT-51™ or DT-AVR Low Cost Series Port 0, 1, 2, A, B, or C (every pin is connected 'straight').
2. To check whether the SSR is active or not, connect the load to each SSR as shown on the example below:



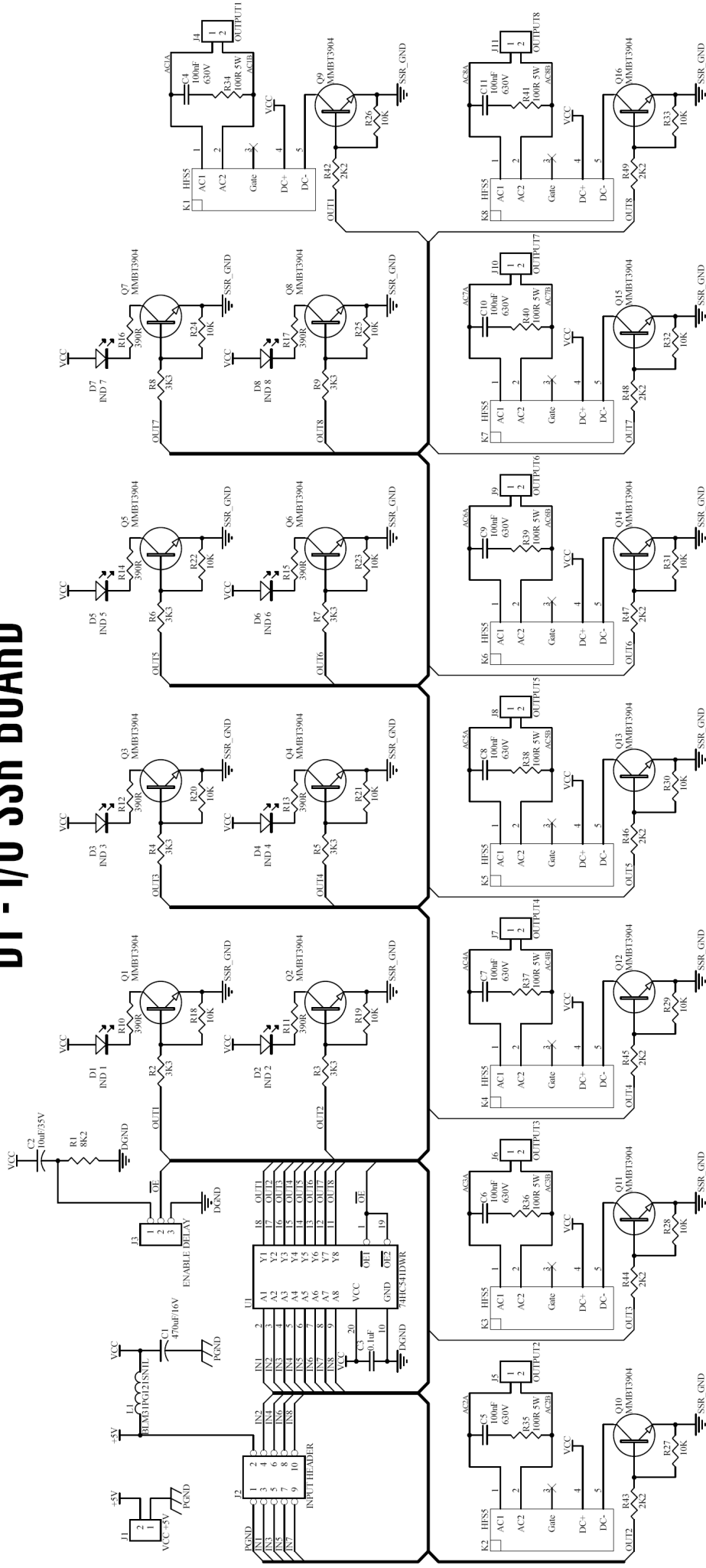
3. Connect the power supply for the the microcontroller module.
4. Perform testing on the microcontroller module to output a square wave on each port (see the microcontroller module manual). Each SSR will be deactivated consecutively.

Testing without microcontroller can be performed by giving logic "1" (+5VDC) on the INx pin to activate the SSR and logic "0" (GND) to deactivate it.

◆ Thank you for your confidence in using our products. If there are difficulties, questions, or suggestions regarding this product, please contact our technical support:

support@innovativeelectronics.com

DT - I/O SSR BOARD



Copyright © 2009 Innovative Electronics